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Certificate no: UC-f0e6e8f6-70d1-4c2a-9c14-cb8881bfa1a3
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Reference Number: 0004

CERTIFICATE OF COMPLETION

Verification Series Part 6 : SystemVerilog Assertions Basics

Instructors **Kumar Khandagle**

Swatipurna Sahoo

Date **July 5, 2025**
Length **10 total hours**