



Certificate no: UC-d5b50efd-9082-42d3-ae13-9e55ea3dc5fa

Certificate url: ude.my/UC-d5b50efd-9082-42d3-ae13-9e55ea3dc5fa

Reference Number: 0004

CERTIFICATE OF COMPLETION

FPGA Embedded Design, Part 1 - Verilog

Instructors **Eduardo Corpeño, Marissa Siliezar**

Md Abdur Rahman

Date **July 15, 2026**

Length **3.5 total hours**