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Certificate no: UC-cf52e2d5-e71c-4ccf-8a13-b26ff44b0b93
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Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

BANDA HARISH BABU

Date **Feb. 27, 2023**
Length **12.5 total hours**