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Certificate no: UC-c077d827-cee5-47fe-8ea3-b2902baa9751
Certificate url: ude.my/UC-c077d827-cee5-47fe-8ea3-b2902baa9751
Reference Number: 0004

CERTIFICATE OF COMPLETION

System Design using Verilog

Instructors **Dr. Yogesh Misra**

Dhananjay Kumar Pandey

Date **July 25, 2021**
Length **25.5 total hours**