



Certificate no: UC-b8b1428a-e340-4a13-997f-bfcbac4a2460

Certificate url: ude.my/UC-b8b1428a-e340-4a13-997f-bfcbac4a2460

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Anirudha Pandey

Date **April 3, 2025**

Length **12.5 total hours**