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Certificate no: UC-b724868d-d1ac-43b3-b186-7b97f2eec274
Certificate url: ude.my/UC-b724868d-d1ac-43b3-b186-7b97f2eec274
Reference Number: 0004

CERTIFICATE OF COMPLETION

System Design using Verilog

Instructors **Dr. Yogesh Misra**

SAUJANYA M S

Date **Jan. 25, 2023**
Length **29 total hours**