



Certificate no: UC-b5a8e14c-afc0-4a27-b5b3-dcd454af3bbf

Certificate url: ude.my/UC-b5a8e14c-afc0-4a27-b5b3-dcd454af3bbf

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

EnYu Huang

Date **Feb. 24, 2022**

Length **12.5 total hours**