



Certificate no: UC-b25aedb8-070a-4a6e-8847-b42fb7caa17d

Certificate url: ude.my/UC-b25aedb8-070a-4a6e-8847-b42fb7caa17d

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Ahmed Albijali

Date **July 24, 2024**

Length **12.5 total hours**