

Certificate of Completion

*This is to certify that Sushant Kumar successfully
completed 36 mins of IC Design Process: A
Beginner's Overview to VLSI Technology online
course on Jan. 30, 2020*

Systemverilog Academy

Systemverilog Academy, Instructor

&



Certificate no: UC-JAVFJV36
Certificate url: ude.my/UC-JAVFJV36

#BeAble