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Certificate no: UC-8bd85df8-95ae-4bca-a814-0f78a90f6f88
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Reference Number: 0004

CERTIFICATE OF COMPLETION

VLSI - Design For Test (DFT)- JTAG, Boundary SCAN and IJTAG

Instructors **VLSI Foundation**

Moulya Raju M T

Date **Oct. 15, 2023**
Length **2 total hours**