



Certificate no: UC-82fbf9b5-3a0c-4781-b738-f04d47918943

Certificate url: ude.my/UC-82fbf9b5-3a0c-4781-b738-f04d47918943

Reference Number: 0004

CERTIFICATE OF COMPLETION

FPGA Embedded Design, Part 1 - Verilog

Instructors **Eduardo Corpeño, Marissa Siliezar**

Venuri Minipaba Amarasinghe

Date **5 Jun 2023**

Length **3.5 total hours**