



Certificate no: UC-7e360ff5-007c-4932-a6cb-390b139b48a0

Certificate url: ude.my/UC-7e360ff5-007c-4932-a6cb-390b139b48a0

Reference Number: 0004

CERTIFICATE OF COMPLETION

FPGA Embedded Design, Part 1 - Verilog

Instructors **Eduardo Corpeño, Marissa Siliezar**

TS Adarsh

Date **20 Mar 2022**

Length **3.5 total hours**