

Certificate of Completion

*This is to certify that **Kulshreshtha** successfully
completed 5 total hours of **Learn SystemVerilog
Assertions and Coverage Coding in-depth** online
course on July 4, 2020*

Ramdas Mozhikunnath M

Ramdas Mozhikunnath M, Instructor

&



Certificate no: UC-7275e22f-a518-413b-86cc-9bf0b19159d4
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#BeAble