Certificate of Completion

*This is to certify that **Manish Kumar** successfully completed 1 total hour of **SystemVerilog Interface - get, set, go!** online course on 28 Jun 2021*

Srinivasan Venkataramanan

Srinivasan Venkataramanan, Instructor

&

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Certificate no: **UC-680f25dd-017d-4888-a8aa-f0feeb1be1d6**
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Version 3

#BeAble