

Certificate of Completion

*This is to certify that **Rajesh Sarkar** successfully
completed 1.5 total hours of **SystemVerilog
Verification -4 : Writing Random TestBench** online
course on March 9, 2020*

Ajith Jose
Ajith Jose, Instructor



Certificate no: UC-56dd64f3-7a10-4b03-9a6f-92da237ba191
Certificate url: ude.my/UC-56dd64f3-7a10-4b03-9a6f-92da237ba191

#BeAble