udemy

Certificate no: UC-4d0aa2c7-4820-4563-9290-abb7b6cac7f0
Certificate url: ude.my/UC-4d0aa2c7-4820-4563-9290-abb7b6cac7f0
Reference Number: 0004

CERTIFICATE OF COMPLETION

ASIC Design & Verification using Verilog HDL +Project Demo

Instructors **VLSI Mentor**

Pradeep M Navalagi

Date **Aug. 30, 2025**
Length **16 total hours**