



Certificate no: UC-4bd544f2-3094-47d2-aa36-d21b29df6eed

Certificate url: ude.my/UC-4bd544f2-3094-47d2-aa36-d21b29df6eed

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Akshay Bavisetty

Date **July 25, 2025**

Length **12.5 total hours**