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Certificate no: UC-4a44bb1c-5b2c-4011-b873-aeebe2b4ddbc
Certificate url: ude.my/UC-4a44bb1c-5b2c-4011-b873-aeebe2b4ddbc
Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Bhavya Gautam

Date **July 11, 2025**
Length **12.5 total hours**