Certificate of Completion

This is to certify that Jimmy Sutaria successfully completed 1 hour of SoC Design 2: Systemverilog Assignment Statements &Synthesis online course on July 12, 2018

System Verilog Academy

System Verilog Academy, Instructor

&

Udemy

Certificate no: **UC-3ZC4SKQ4**
Certificate url: **ude.my/UC-3ZC4SKQ4**

#BeAble