



Certificate no: UC-27bffc8-2c0d-41d5-97c4-f4a072382152

Certificate url: ude.my/UC-27bffc8-2c0d-41d5-97c4-f4a072382152

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Bishnu Chetry

Date **July 2, 2024**

Length **12.5 total hours**