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Certificate no: UC-2388d8c2-a9ba-48d8-86f2-fea9a3f3f5a9
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Reference Number: 0004

CERTIFICATE OF COMPLETION

ASIC Synthesis-STA-Physical Design(PD):Cadence+Synopsys flow

Instructors **VLSI Mentor**

Yash Siriah

Date **July 15, 2026**
Length **41 total hours**