



Certificate no: UC-1df5a205-9167-4fa0-8825-e9544a7c700d

Certificate url: ude.my/UC-1df5a205-9167-4fa0-8825-e9544a7c700d

Reference Number: 0004

CERTIFICATE OF COMPLETION

VLSI - Design For Test (DFT)- JTAG, Boundary SCAN and IJTAG

Instructors **VLSI Foundation**

Parth Goyal

Date **July 21, 2023**

Length **2 total hours**