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Certificate no: UC-19081d5c-a6ba-4456-99ec-67a5f8ff65f3

Certificate url: ude.my/UC-19081d5c-a6ba-4456-99ec-67a5f8ff65f3

Reference Number: 0004

CERTIFICATE OF COMPLETION

System Design using Verilog

Instructors **Dr. Yogesh Misra**

Dhiraj Gurjar

Date **Sept. 24, 2026**

Length **30.5 total hours**