Certificate of Completion

This is to certify that **Edgar Sáenz Zubía** *successfully completed 7.5 total hours of* **Crea tu primer circuito con VHDL y FPGA** *online course on April 28, 2020*

Javier Castillo
Javier Castillo, Instructor

José Ignacio Martínez
José Ignacio Martínez, Instructor

&

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